

**RESPONSE UNDER 37 CFR 1.116  
EXPEDITED PROCEDURE - EXAMINING GROUP 2810**

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Abhishek Lal  
Application No. : 10/615,601  
Filed : July 7, 2003  
For : BINARY DECODERS IN ELECTRONIC INTEGRATED  
CIRCUITS

Examiner : Jean Bruner Jeanglaude  
Art Unit : 2819  
Docket No. : 852463.402  
Date : April 23, 2007

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE UNDER 37 CFR 1.116**

Commissioner for Patents:

In response to the Office Action dated October 24, 2006, please extend the period of time for response three months, to expire on April 24, 2007. Enclosed are a Petition for an Extension of Time and the requisite fee. Please amend the application as follows:

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks** begin on page 6 of this paper.